

ABSTRACT OF THE DISCLOSURE

There is provided a display which can prevent image deterioration. The display is provide with a shift register circuit including a first circuit part comprising a first transistor of first conductivity type connected to a first potential side and turned on in response to a clock signal, a second transistor of first conductivity type connected to a second potential side, a third transistor of first conductivity type connected between a gate of the first transistor and the second potential and a high resistance connected between the gate of the first transistor and a clock signal line.